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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 2

<i>Complete if Known</i>		
Application / Conf. N.	10/082,630	/ 4447
Filing Date	February 22, 2002	
First Named Inventor	Robert Yin	
Art Unit	2819	
Examiner Name	Unknown	
Attorney Docket Number	X-1070 US	

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

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Signature**

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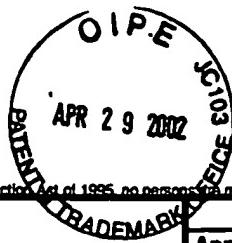
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Sheet 2 of 2

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application / Conf. No.	10/082,630 / 4447
Sheet	2	of	2	Filing Date	February 22, 2002
				First Named Inventor	Robert Yin
				Art Unit	2819
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Sheet

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Application / Conf. N .	10/082,630	/	4447
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Attorney Docket Number

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OTHER – NON PATENT LITERATURE DOCUMENTS			
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<i>SJF</i>	/	SAYFE KIAEI et al., "VLSI DESIGN OF DYNAMICALLY RECONFIGURABLE ARRAY PROCESSOR-DRAP" IEEE, February 1989, pp. 2484-2488, V3.6, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	T
<i>SJF</i>	/	VASON P. SRINI, "FIELD PROGRAMMABLE GATE ARRAY (FPGA) IMPLEMENTATION OF DIGITAL SYSTEMS: AN ALTERNATIVE TO ASIC," IEEE, May 1991, pp. 309-314, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
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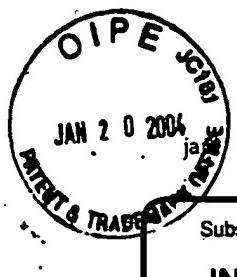
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application/Cnf. No.	10/082,630 / 4447
				Filing Date	February 22, 2002
				First Named Inventor	Robert Yin
				Art Unit	2819
				Examiner Name	Unknown
(use as many sheets as necessary)				Attorney Docket Number	X-1070 US
Sheet	3	of	7		

U.S. PATENT DOCUMENTS

U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No.'	Document Number Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		US- 5,742,180	04-21-98	DeHon et al.	
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		US- 6,026,481	02-15-00	New et al.	
		US- 5,705,938	01-06-98	Kean	JAN 22 2004
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		US- 6,483,342	11-19-02	Britton et al.	Technology Center 2100
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Applicant/Cnf. N.	10/082,630 / 4447
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First Named Inventor	Robert Yin
Art Unit	2819
Examiner Name	Unknown
Attorney Docket Number	X-1070 US

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RJ	1	XILINX, INC., "THE PROGRAMMABLE LOGIC DATA BOOK," 1994, Revised 1995, pp 2-109 to 2-117, Xilinx, Inc., 2100 Logic Drive, San Jose, CA. 95124.	
RJ	1	XILINX, INC., "THE PROGRAMMABLE LOGIC DATA BOOK," 1994, Revised 1995, pp 2-9 to 2-18; 2-187 to 2-199, Xilinx, Inc., 2100 Logic Drive, San Jose, CA. 95124.	
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Sheet 5 of 7 Attorney Docket Number

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		Number - Kind Code (if known)			
		US- 6,011,407	01-04-00	New	
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RY		XILINX, INC., "THE PROGRAMMABLE LOGIC DATA BOOK," 2000, Ch 3, pp 3-7 TO 3-17; 3-76 TO 3-87, Xilinx, Inc., 2100 Logic Drive, San Jose, CA. 95124.	
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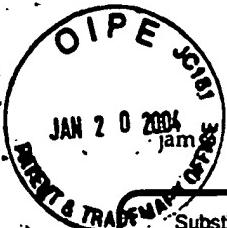
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		US- 6,519,753	02-11-03	Ang
		US- 6,434,735	08-13-02	Watkins
		US- 6,588,006	07-01-03	Watkins
		US- 6,539,508	03-25-03	Patrie et al.
		US- 6,587,995	07-01-03	Duboc et al.
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		US- 09/917,304	07-27-01	Douglass et al.
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